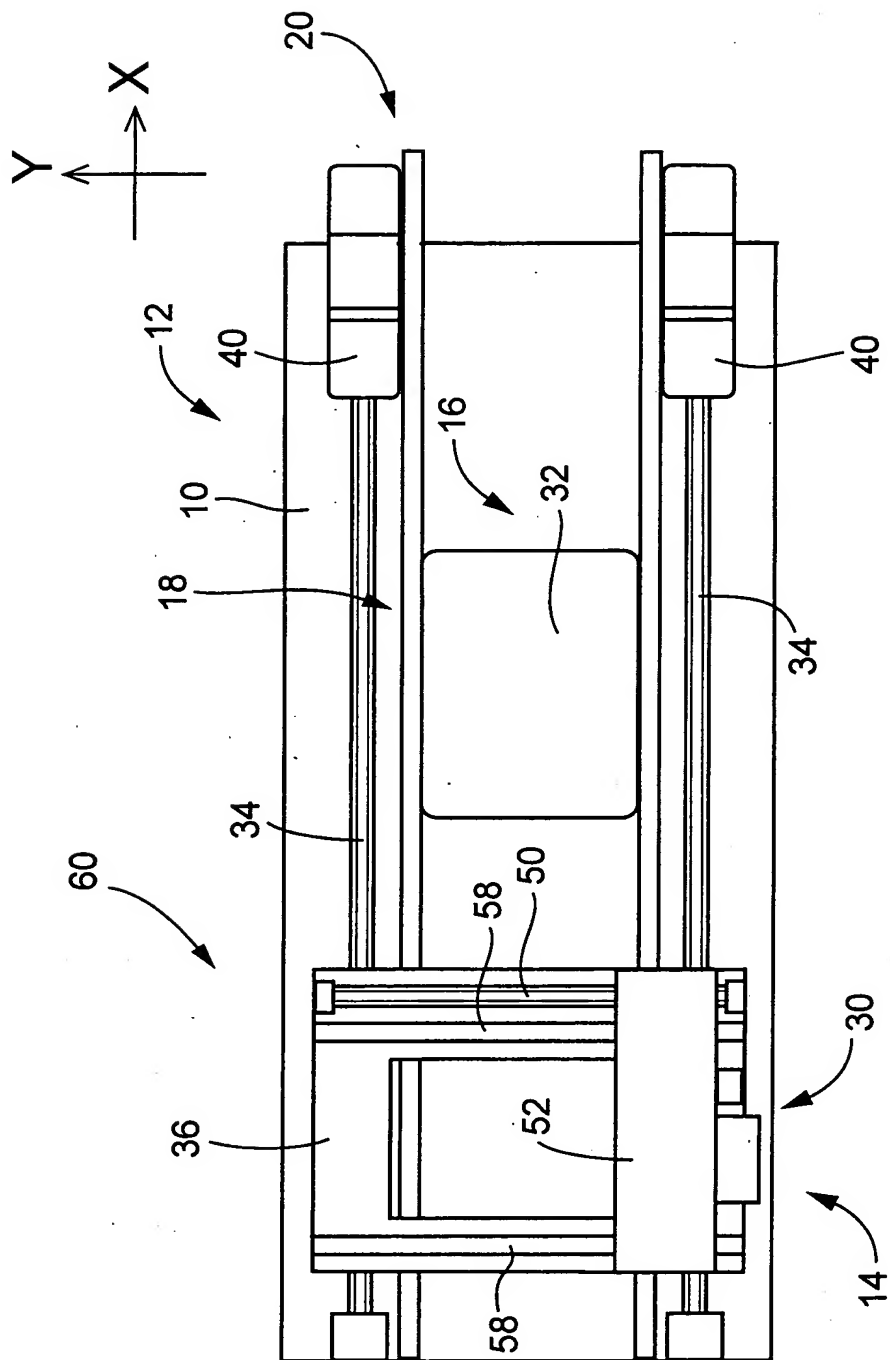


FIG. 1



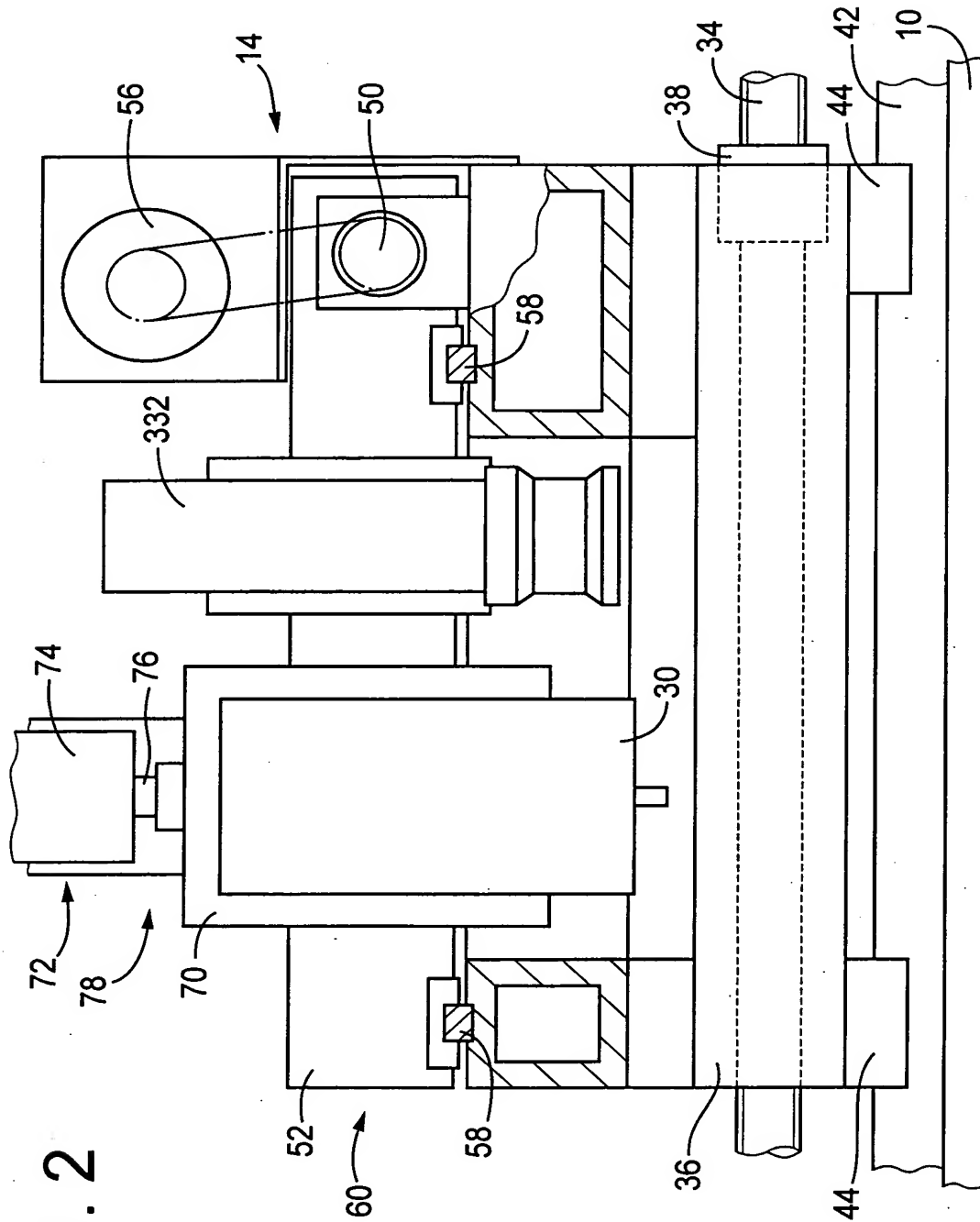
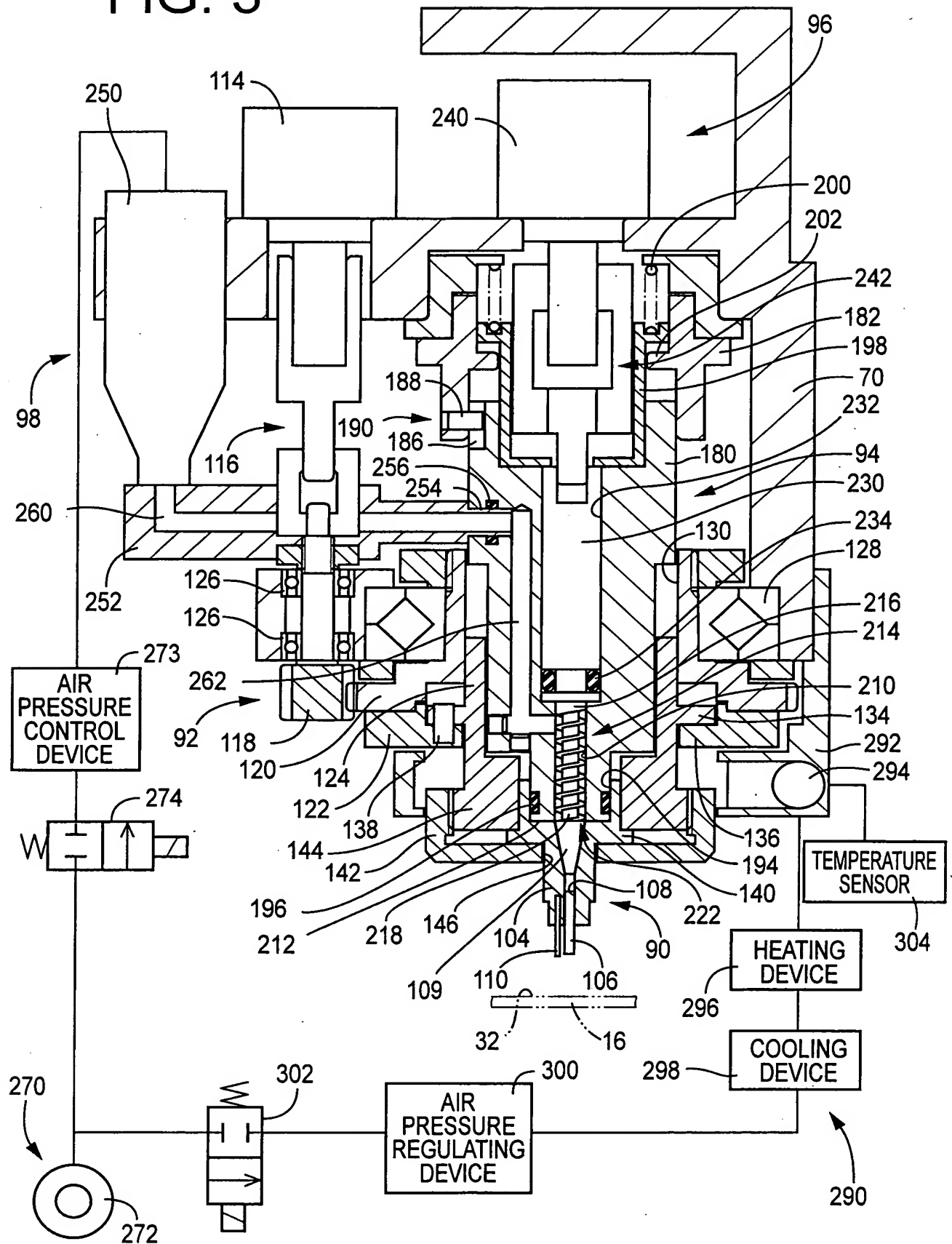


FIG. 3



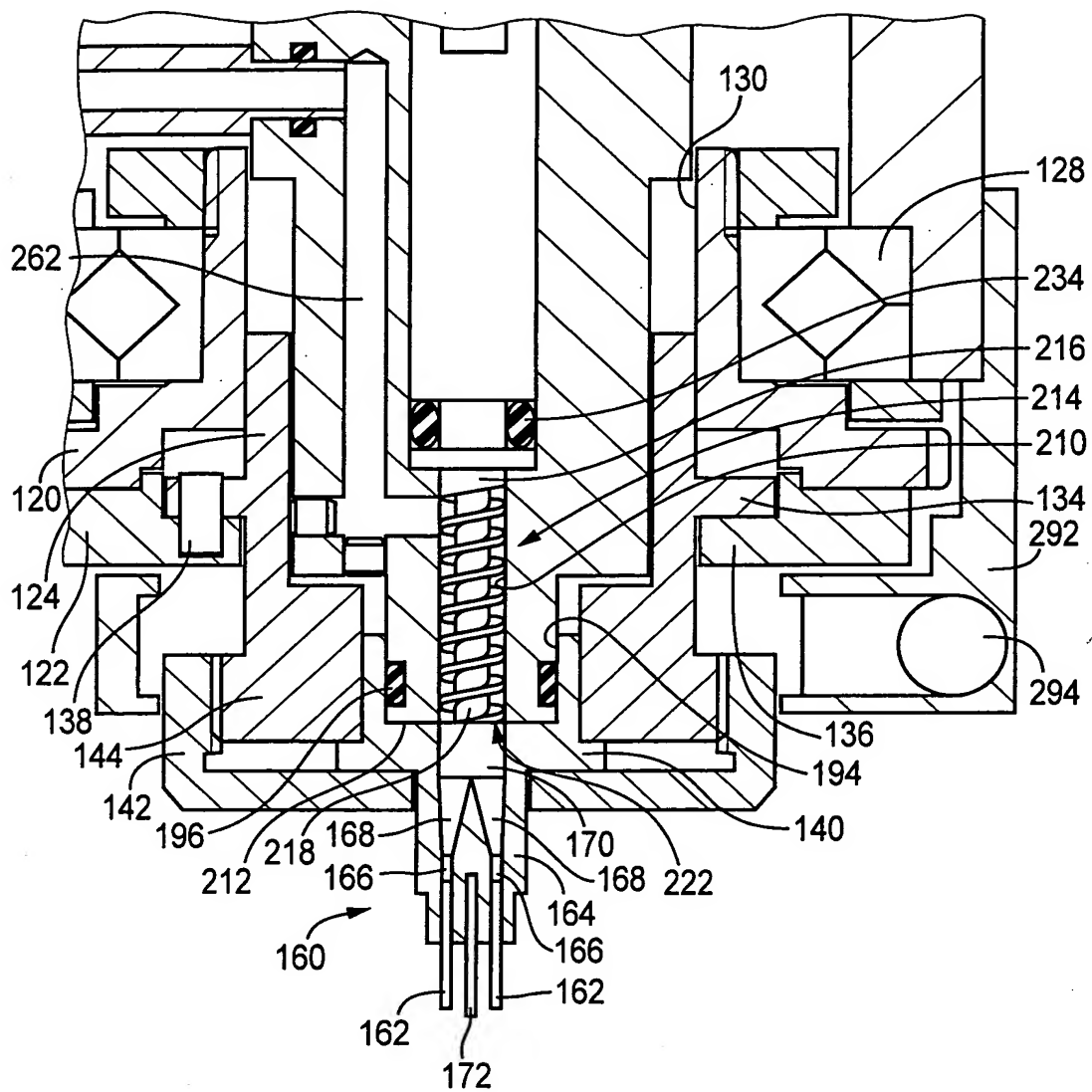


FIG. 4

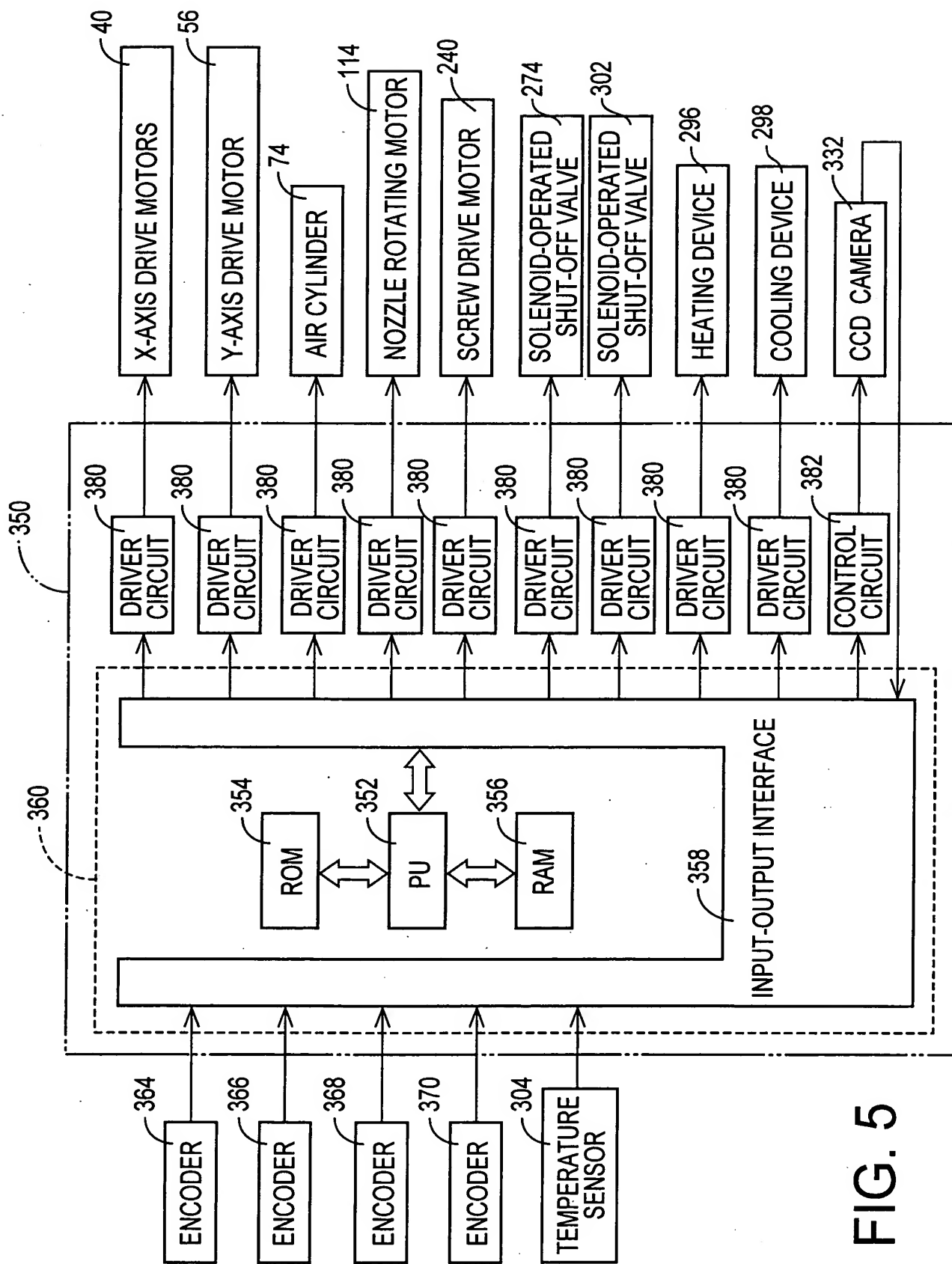
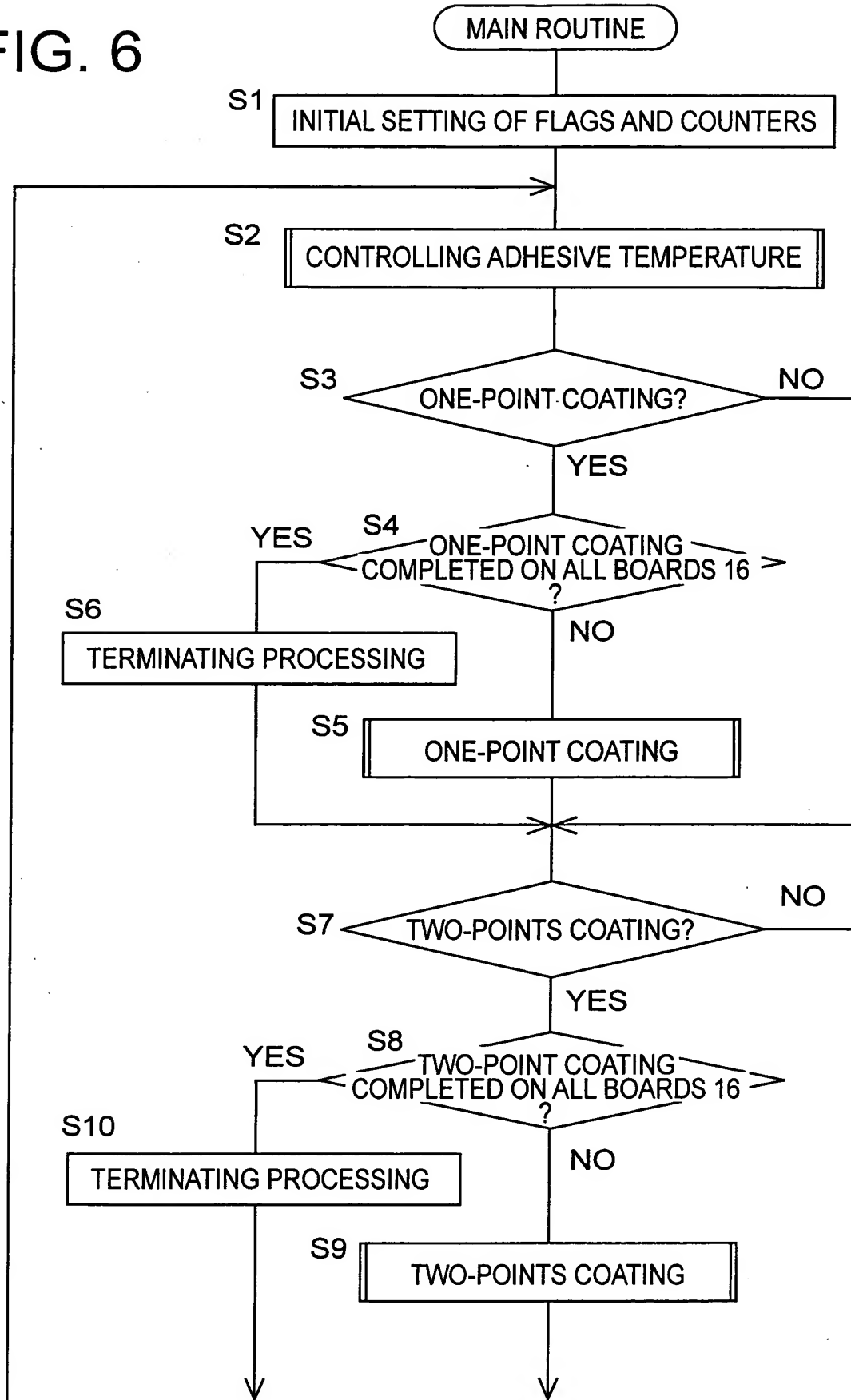


FIG. 5

FIG. 6



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FIG. 7

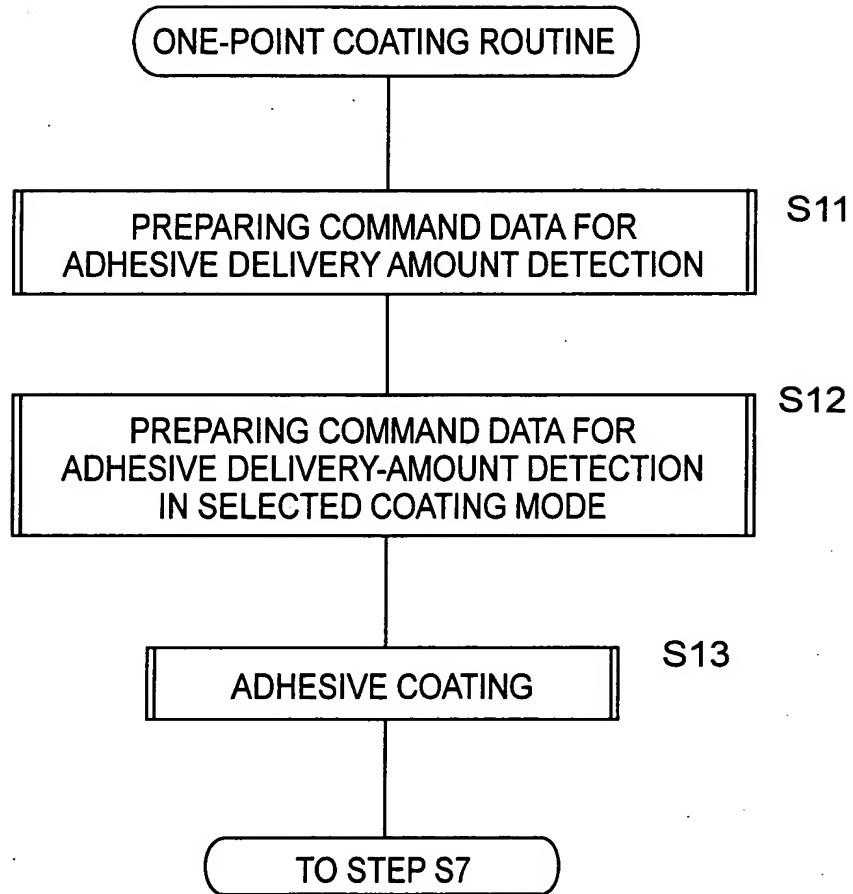


FIG. 8

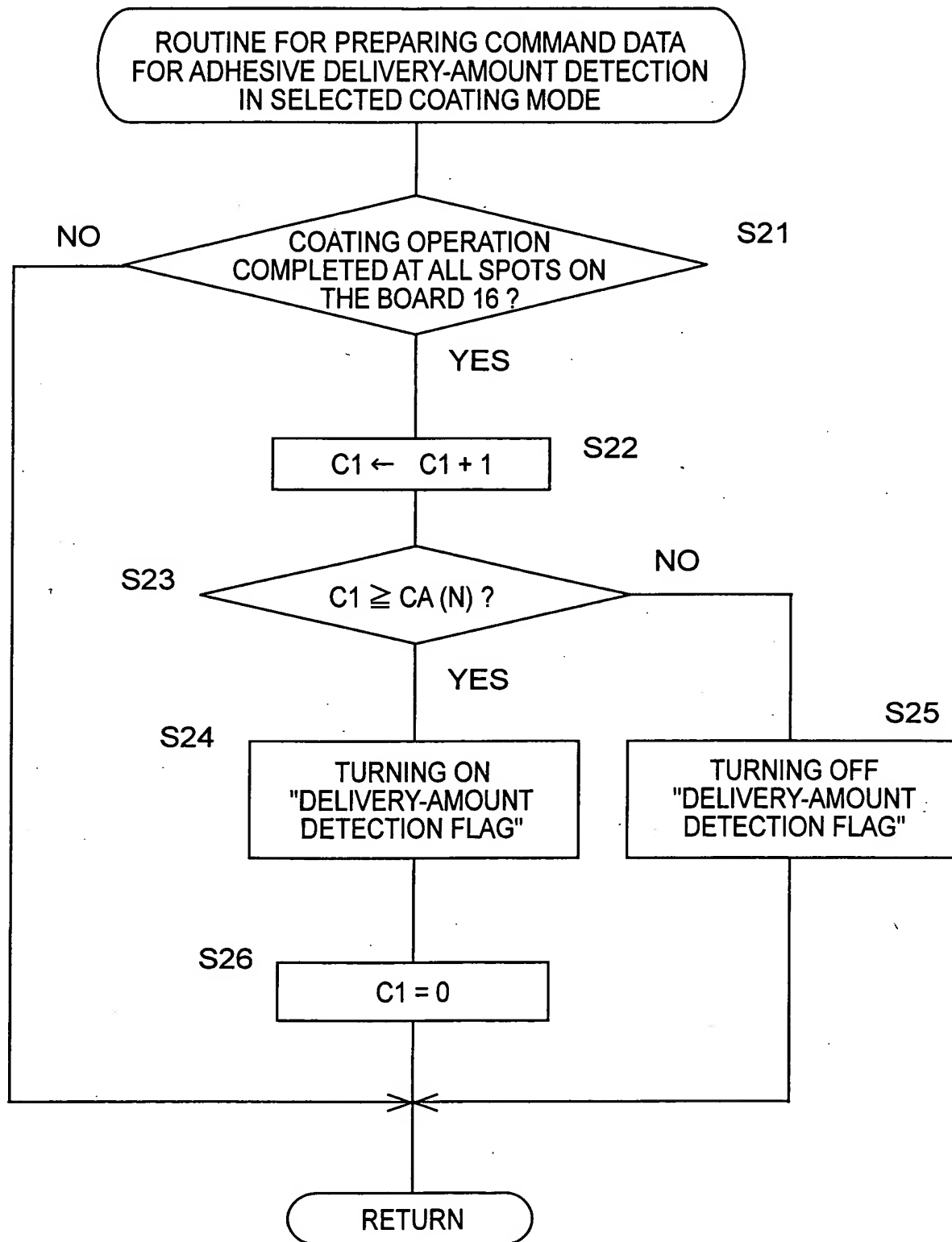
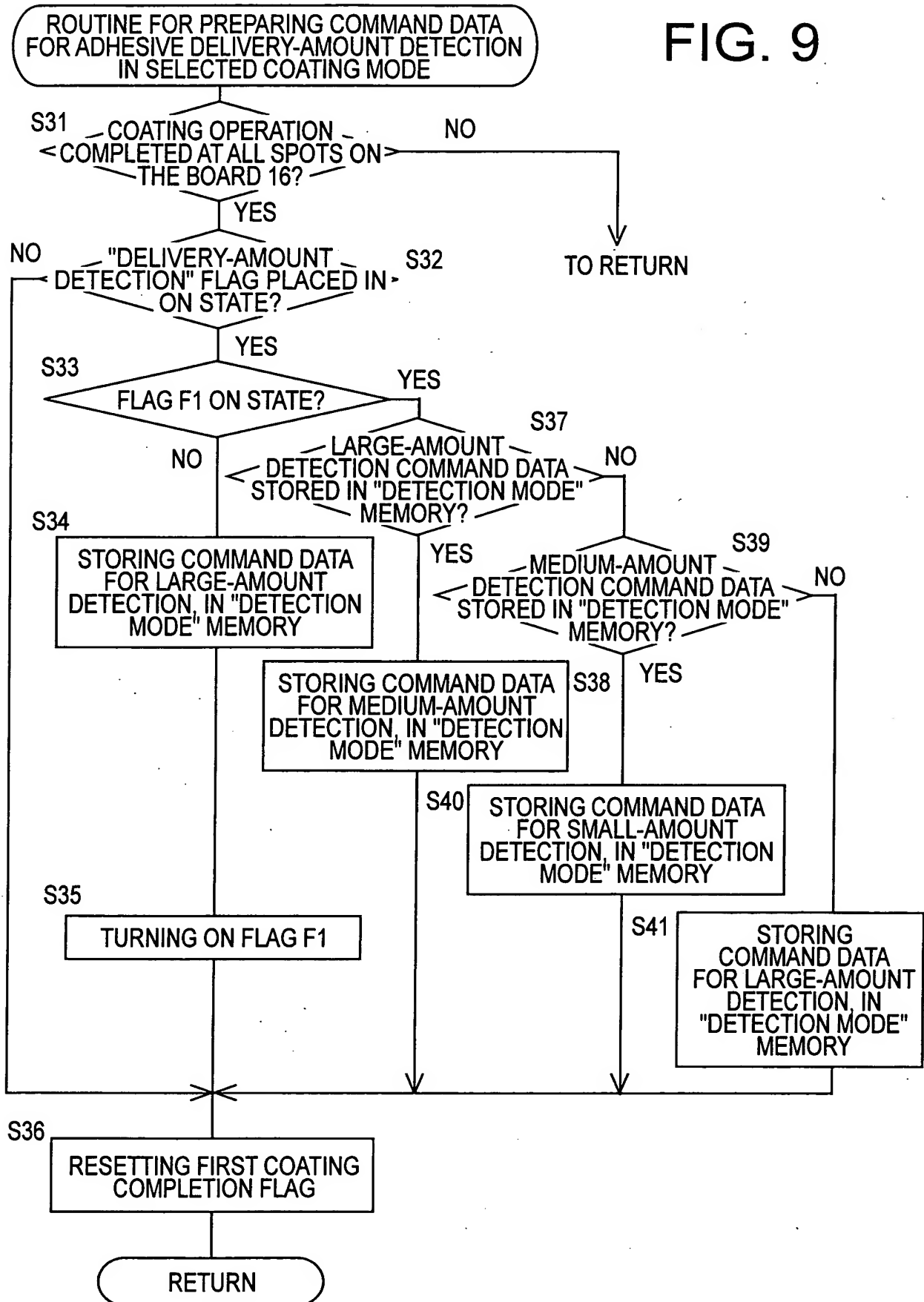


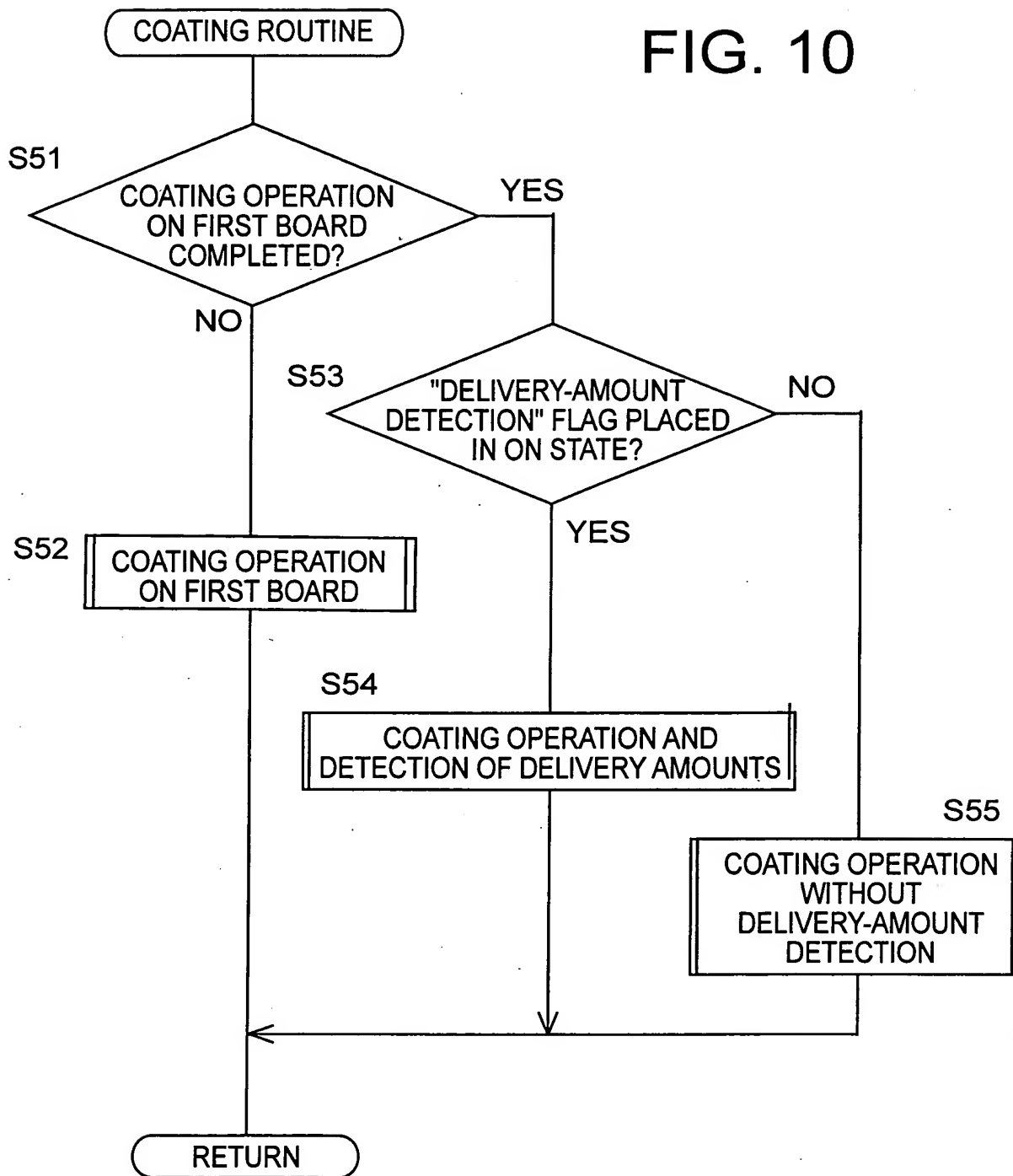


FIG. 9



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FIG. 10



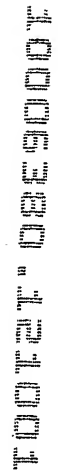
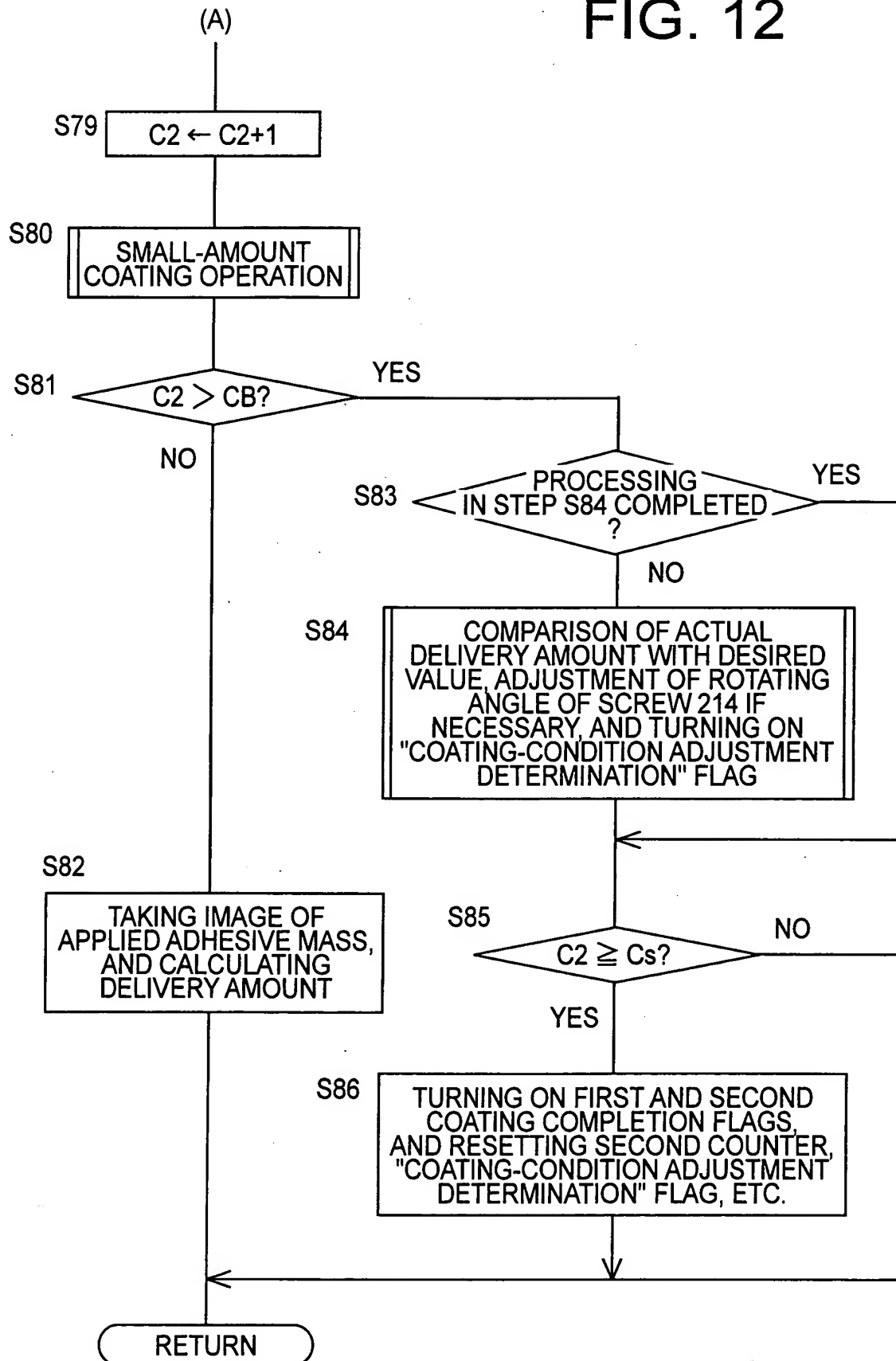


FIG. 12



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FIG. 13

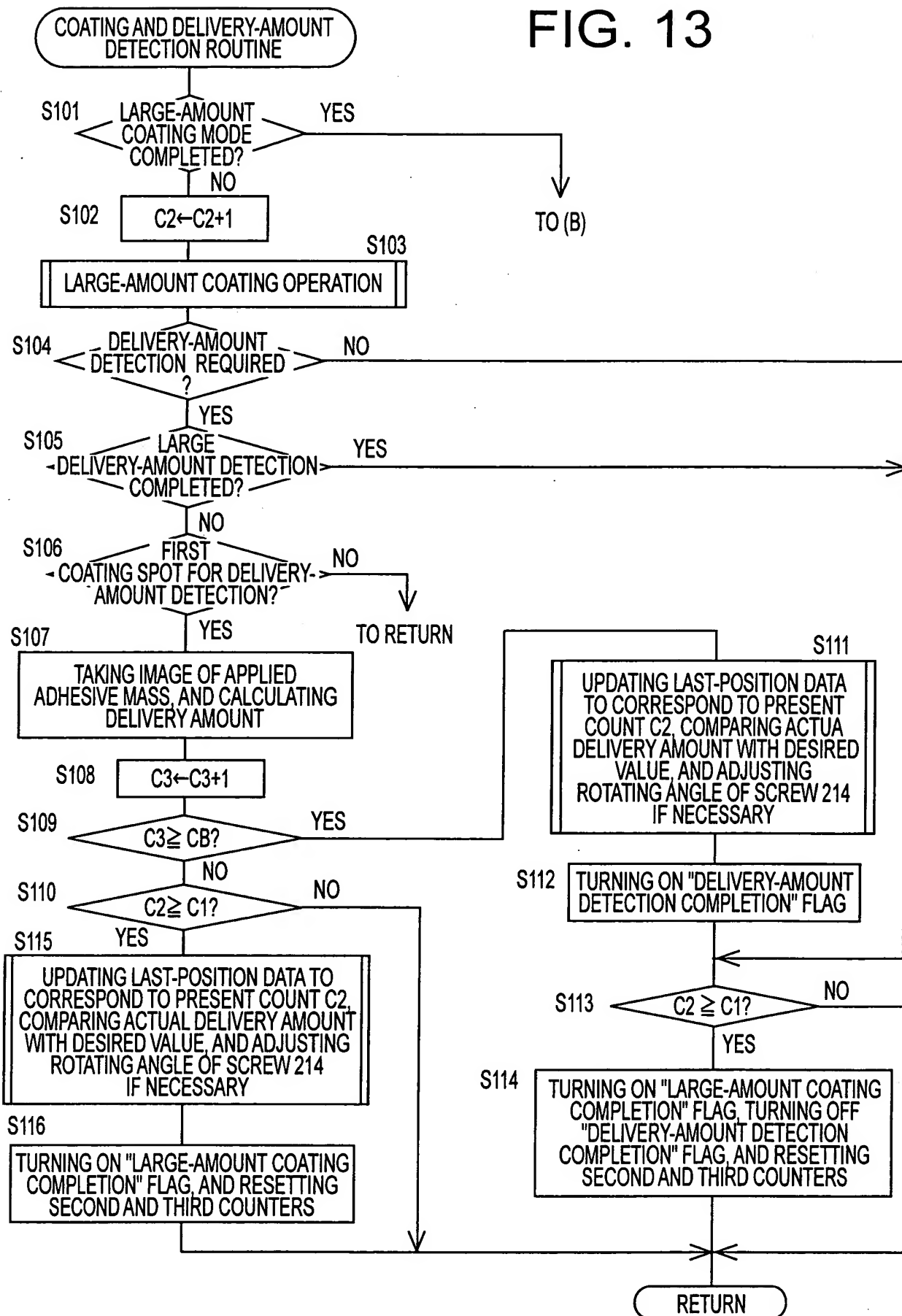
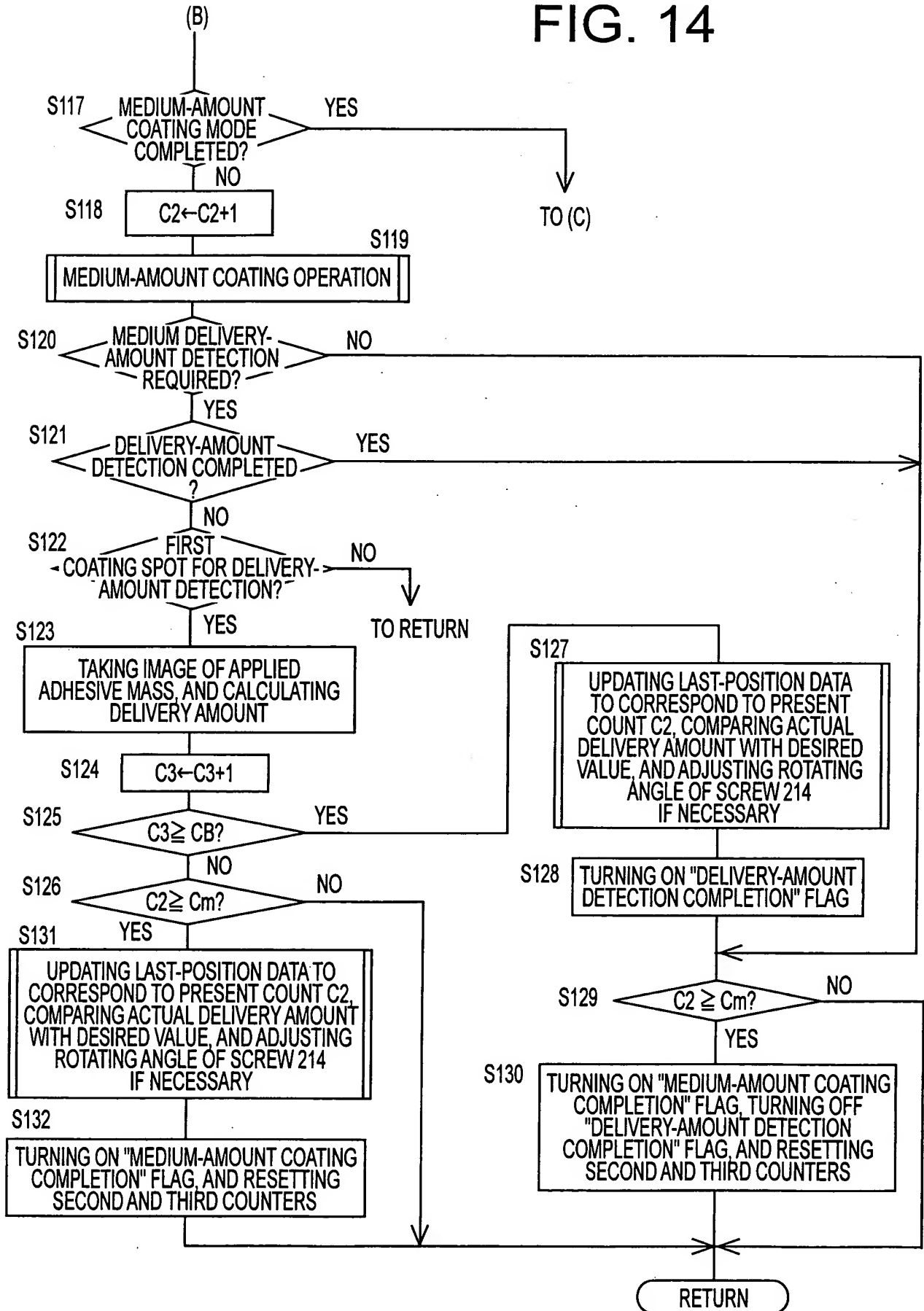
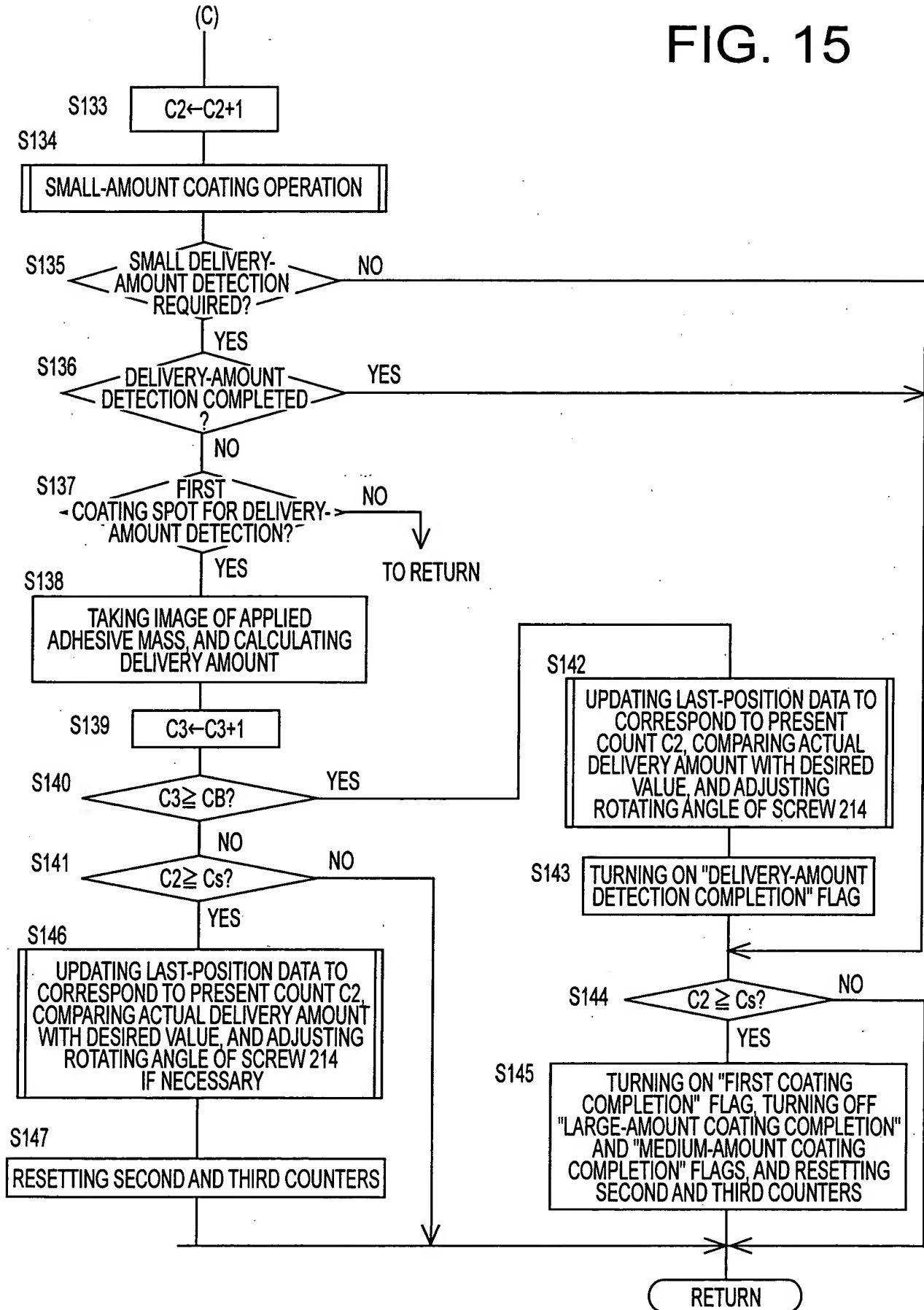


FIG. 14



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FIG. 15



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ROUTINE FOR COATING WITHOUT  
DELIVERY-AMOUNT DETECTION

FIG. 16

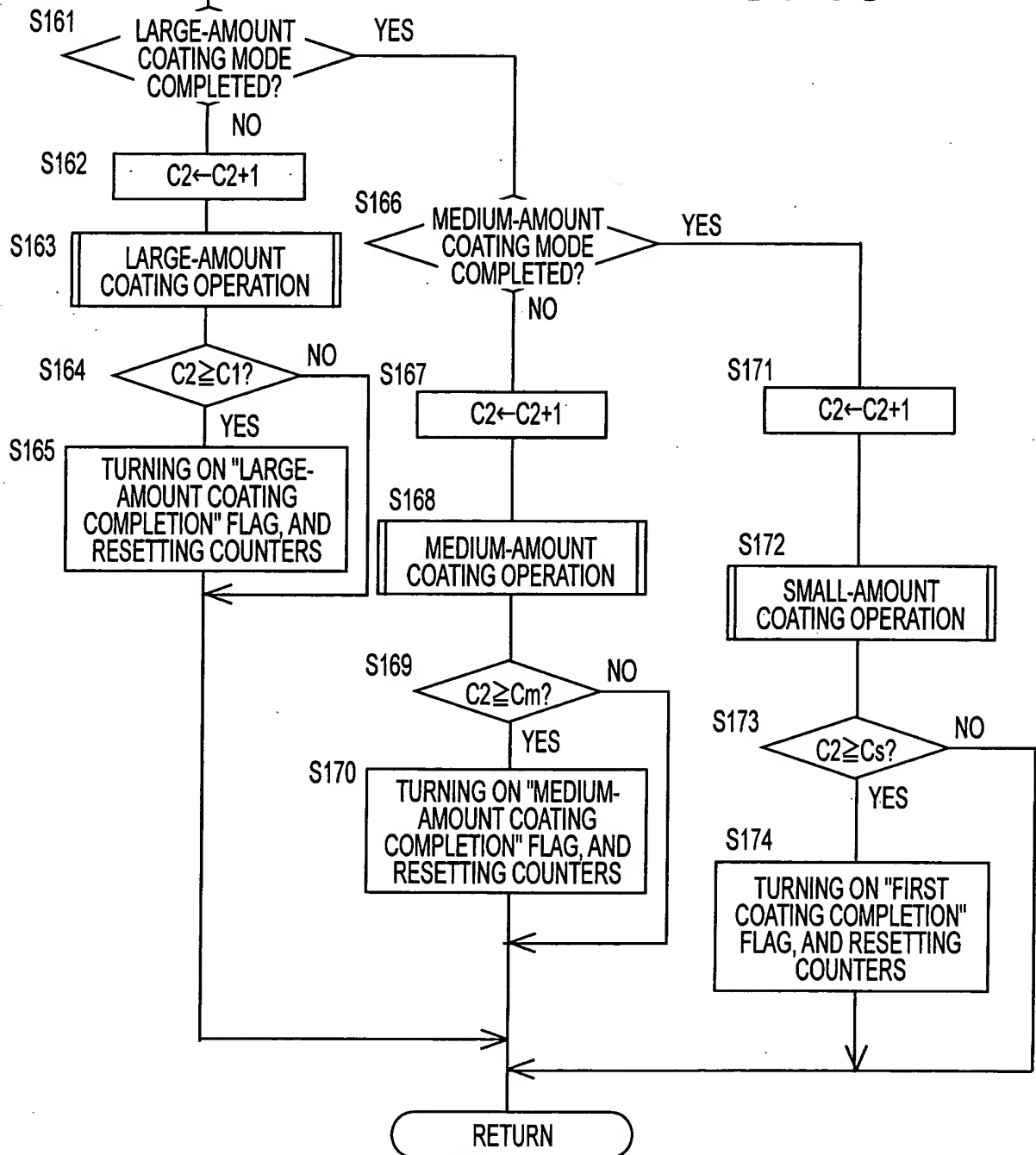
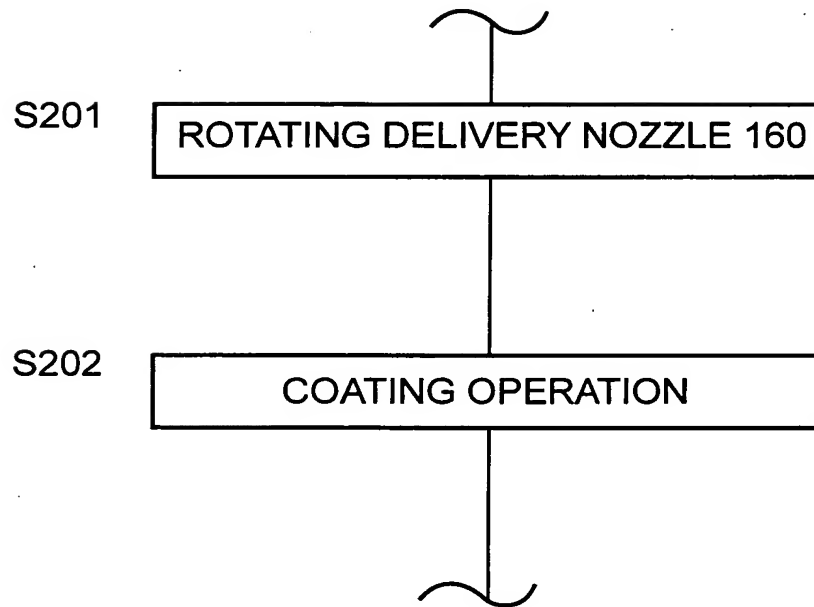




FIG. 17



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# FIG. 18

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## RAM

DELIVERY-AMOUNT DETECTION MODE MEMORY	CONTROL PROGRAM MEMORY
LARGE DELIVERY-AMOUNT MEMORY	COATING POINT-NUMBER MEMORY
MEDIUM DELIVERY-AMOUNT MEMORY	DELIVERY AMOUNT MEMORY
SMALL DELIVERY-AMOUNT MEMORY	COATING CONDITION ADJUSTMENT DETERMINATION FLAG
LAST LARGE-AMOUNT DETECTING POSITION MEMORY	DELIVERY-AMOUNT DETECTION COMPLETION FLAG
LAST MEDIUM-AMOUNT DETECTING POSITION MEMORY	FLAG F1
LAST SMALL-AMOUNT DETECTING POSITION MEMORY	FIRST COUNTER
FIRST COATING COMPLETION FLAG	SECOND COUNTER
SECOND COATING COMPLETION FLAG	THIRD COUNTER
DELIVERY-AMOUNT DETECTION FLAG	
LARGE-AMOUNT COATING COMPLETION FLAG	
MEDIUM-AMOUNT COATING COMPLETION FLAG	

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FIG. 19

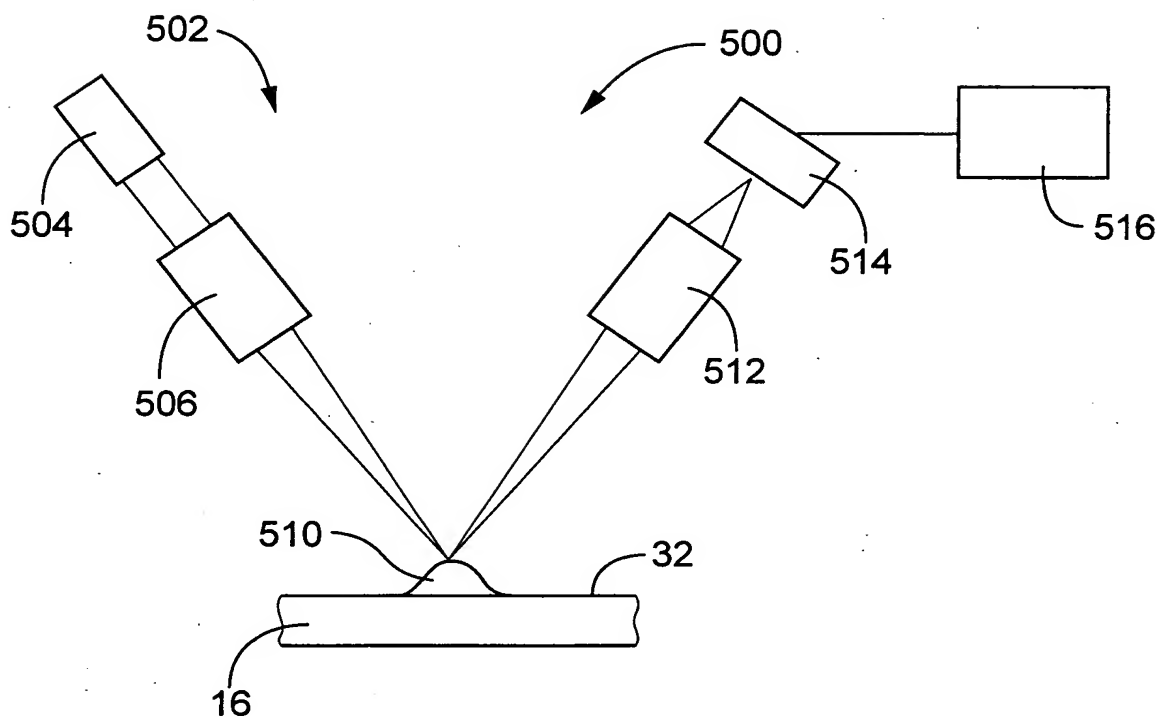


FIG. 20

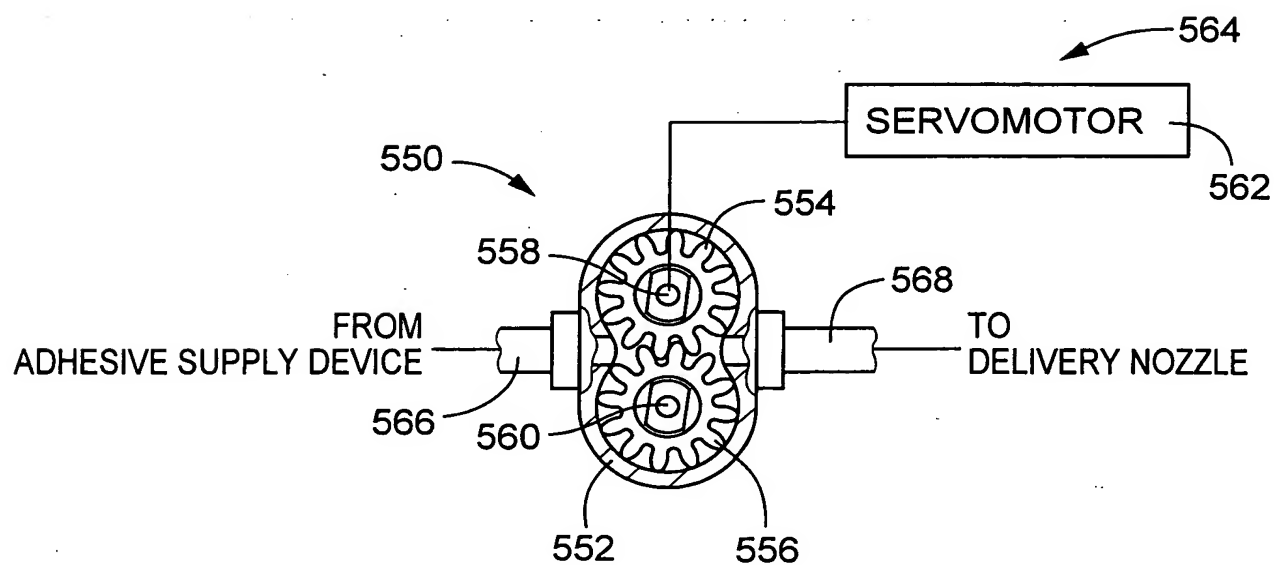
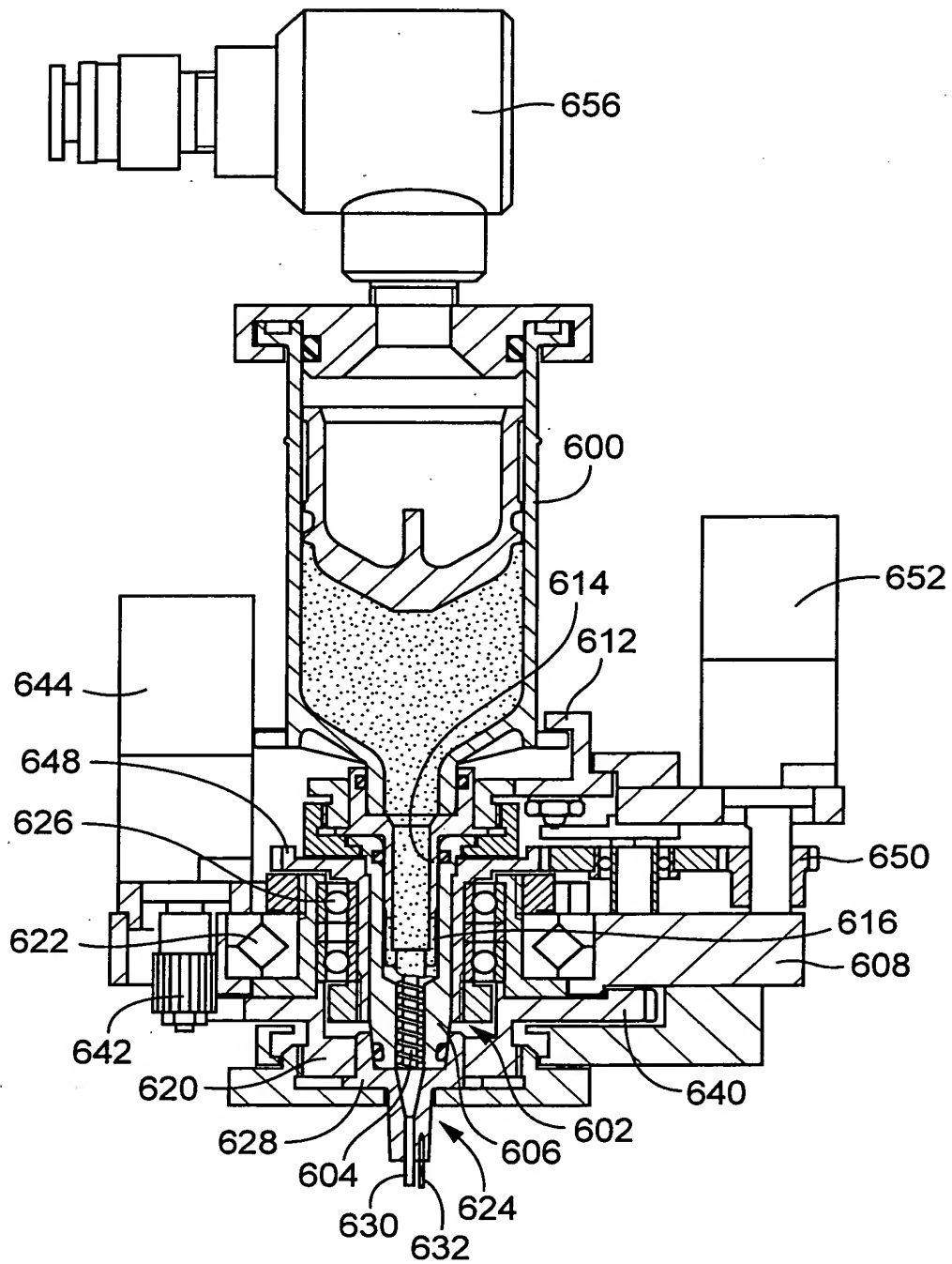


FIG. 21



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